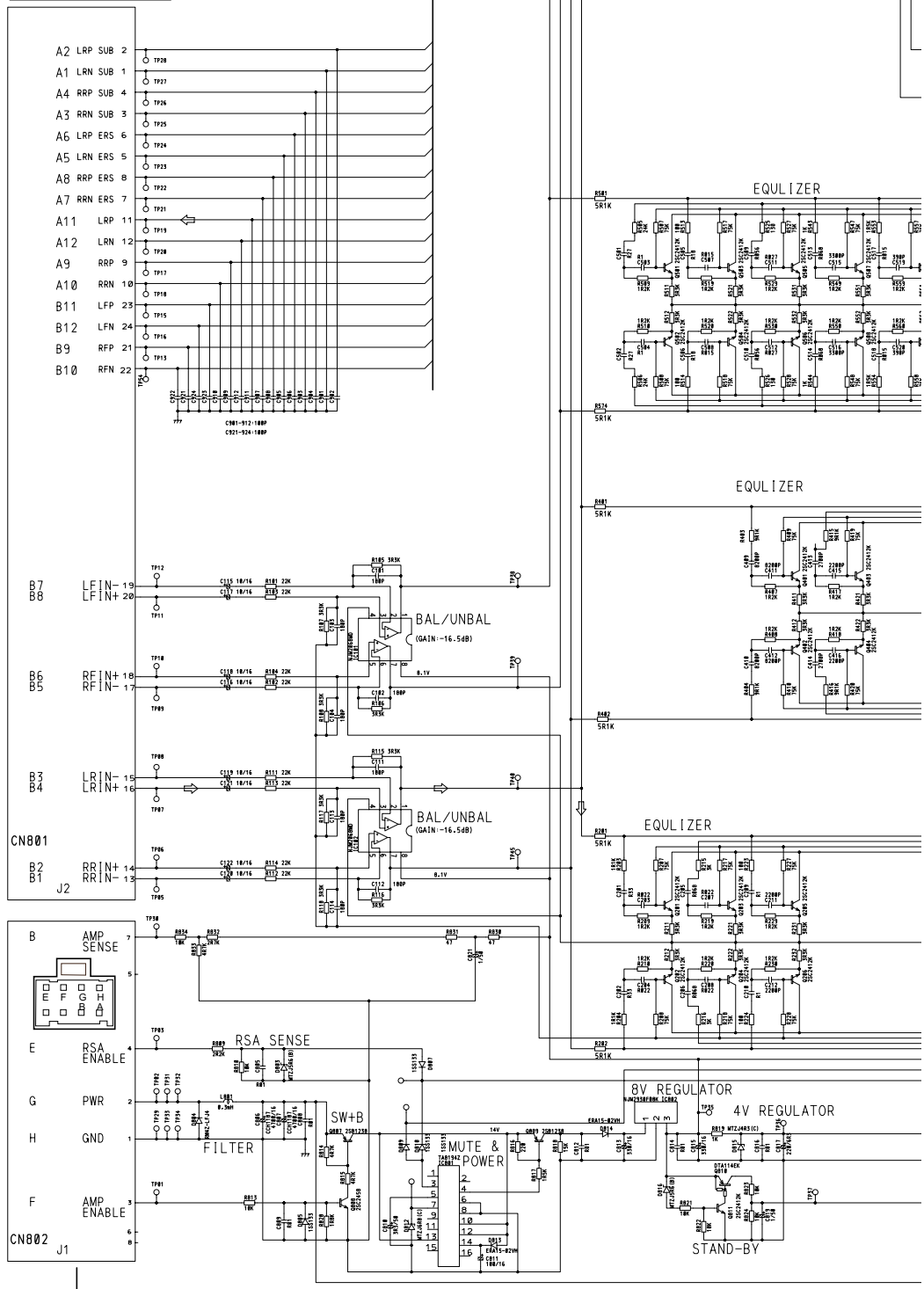
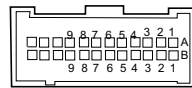
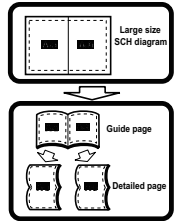
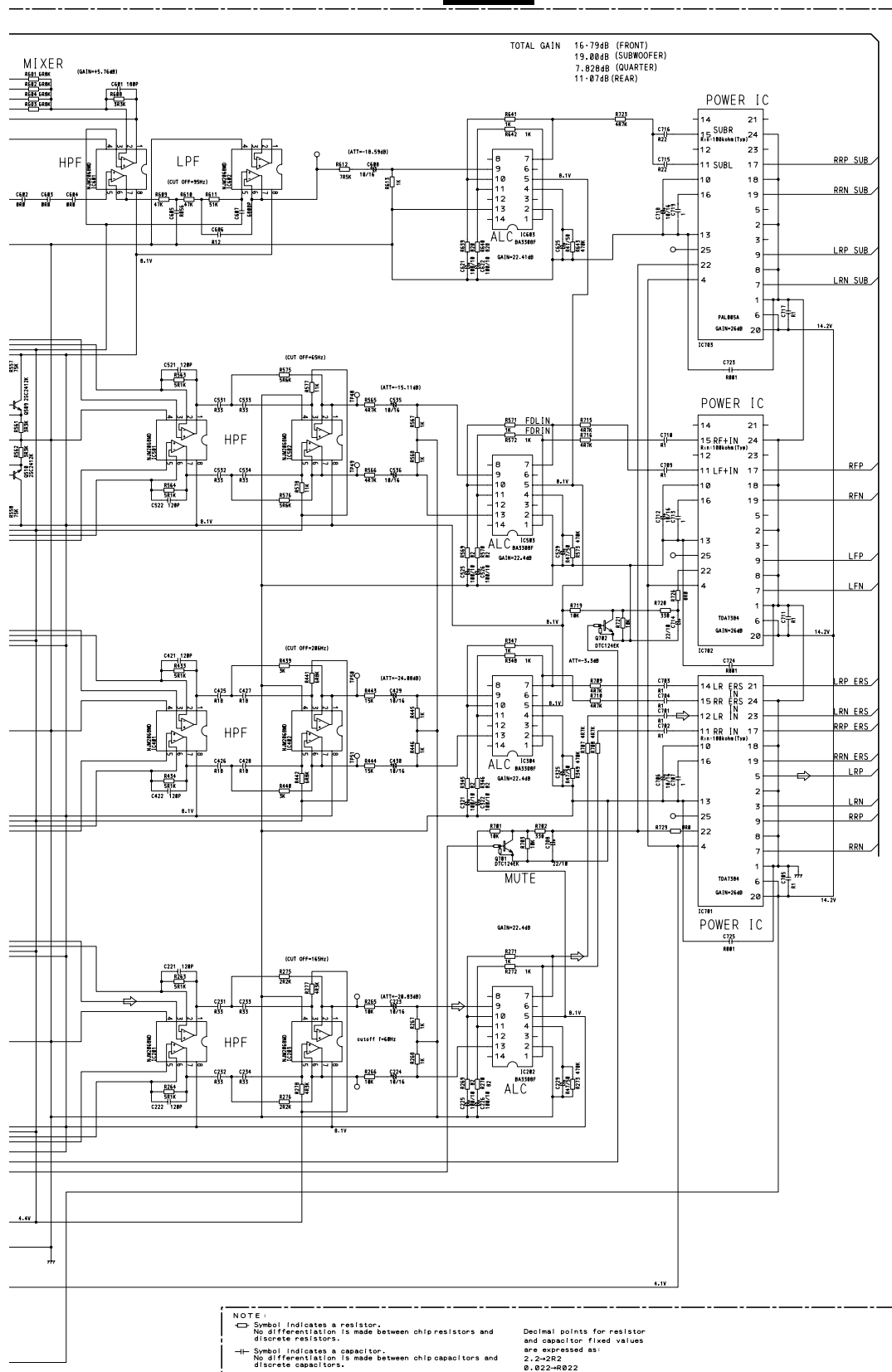


SCHEMATIC DIAGRAM

OVERALL CONNECTION DIAGRAM(GUIDE PAGE)

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A-a**A** MOTHER UNIT**A**

A-b



A

B

C

D

A

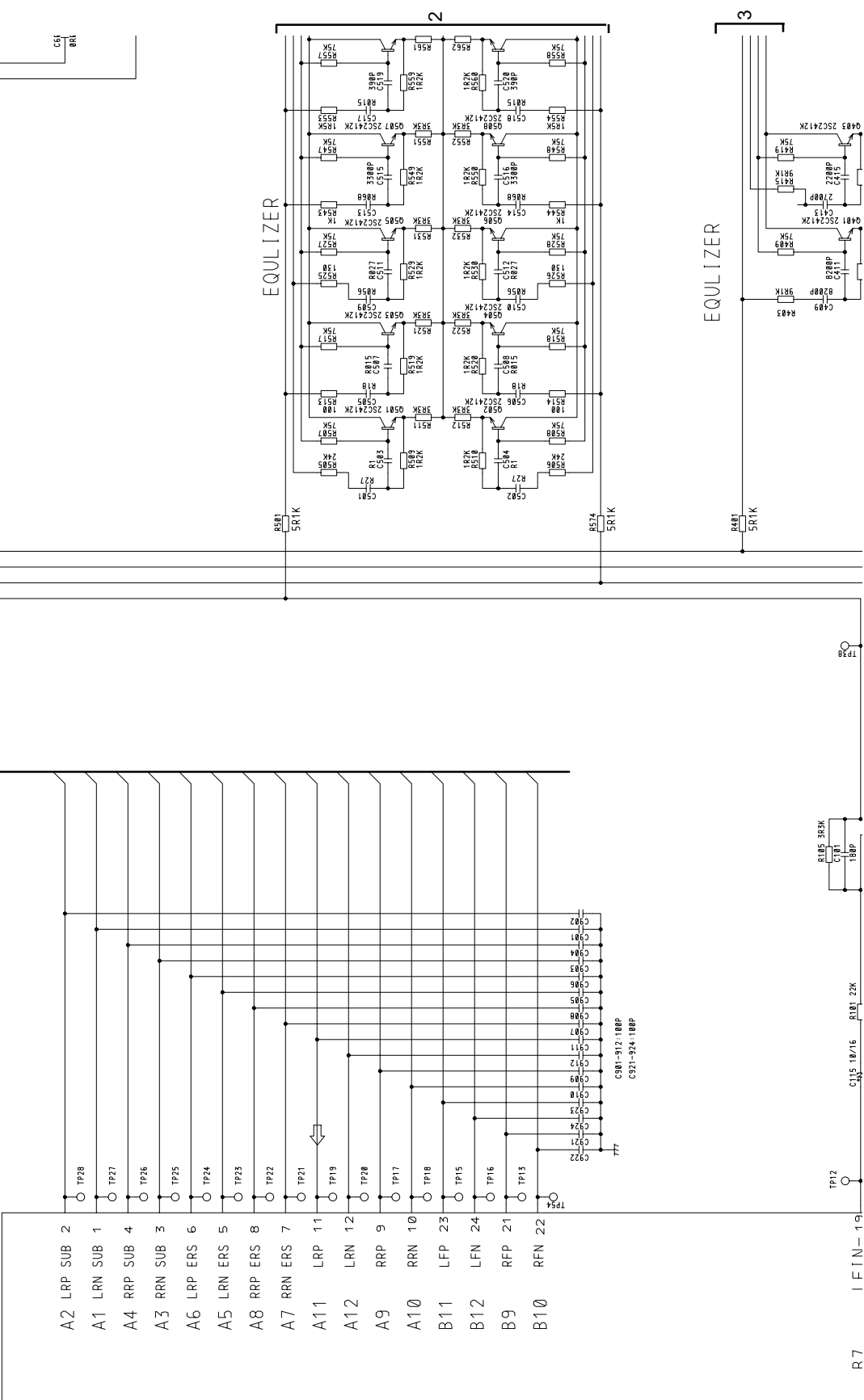
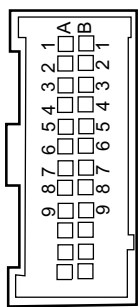
B

C

D

A-a A-b

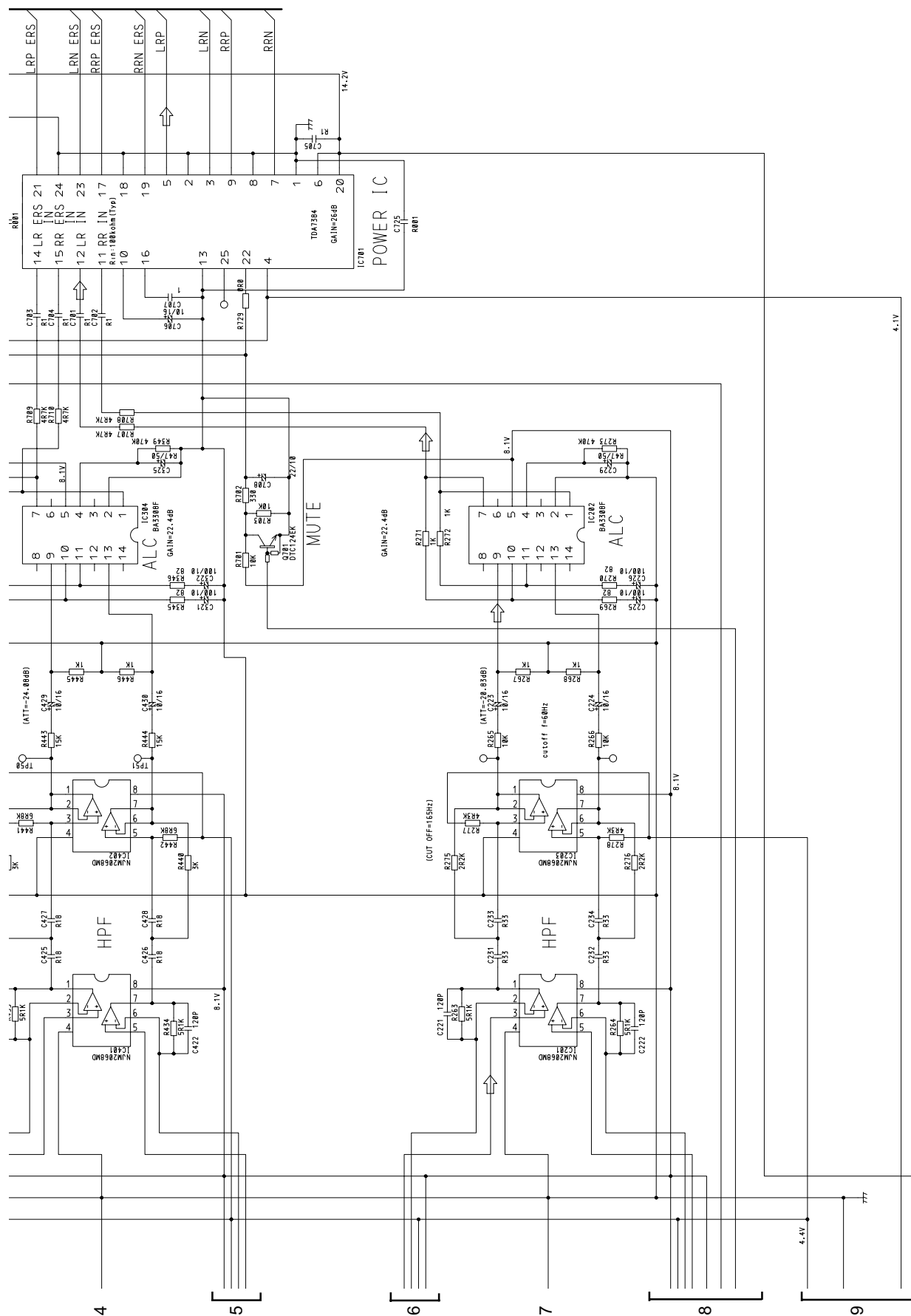
A MOTHER UNIT





A-a A-b





NOTE:

- Symbol indicates a resistor. No differentiation is made between chip resistors and discrete resistors.
- Symbol indicates a capacitor. No differentiation is made between chip capacitors and discrete capacitors.
- Decimal points for resistor and capacitor fixed values are expressed as: 2.2-2R2 0.022-0R22

A-a A-b

A

B

C

D