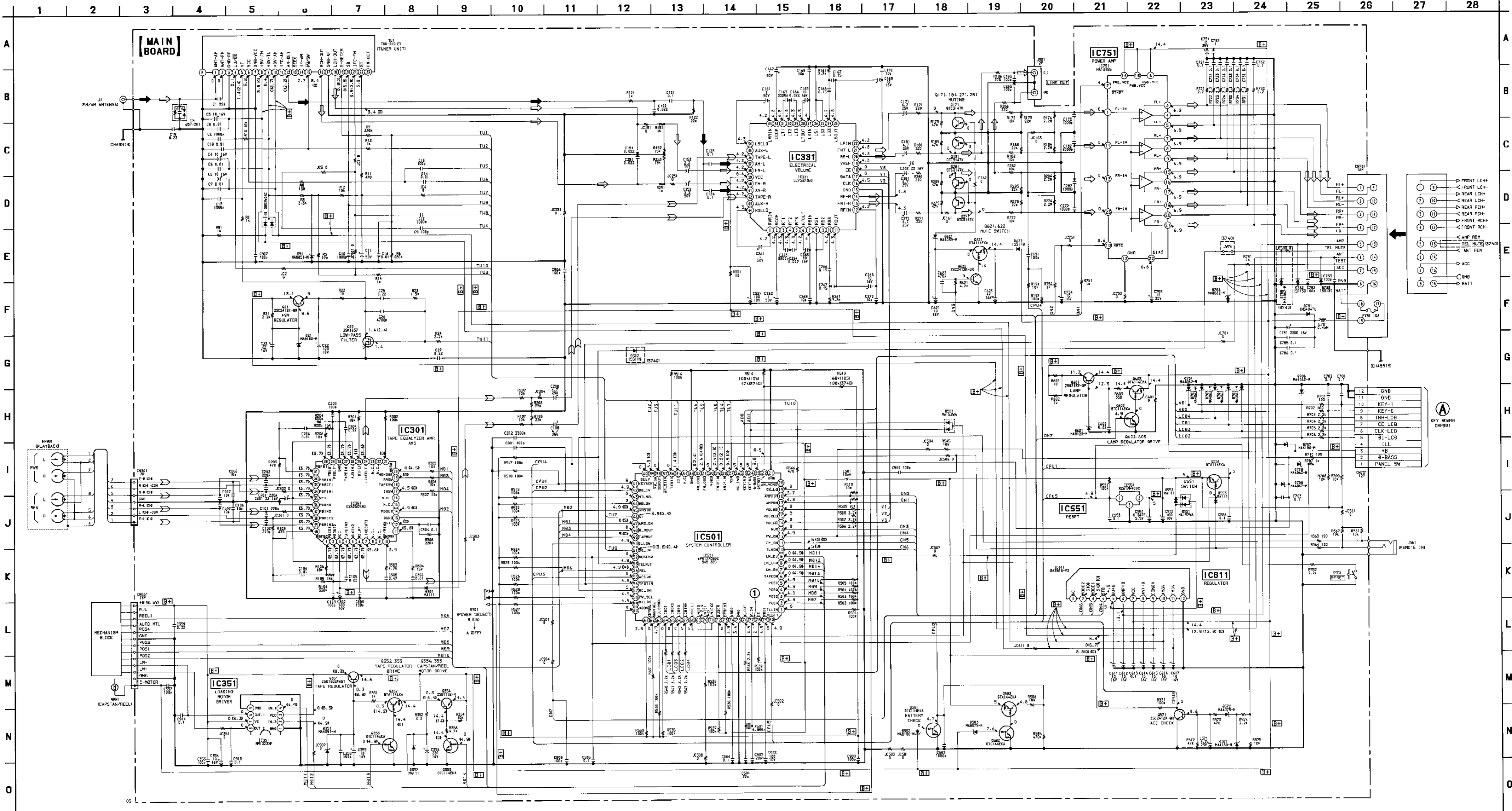


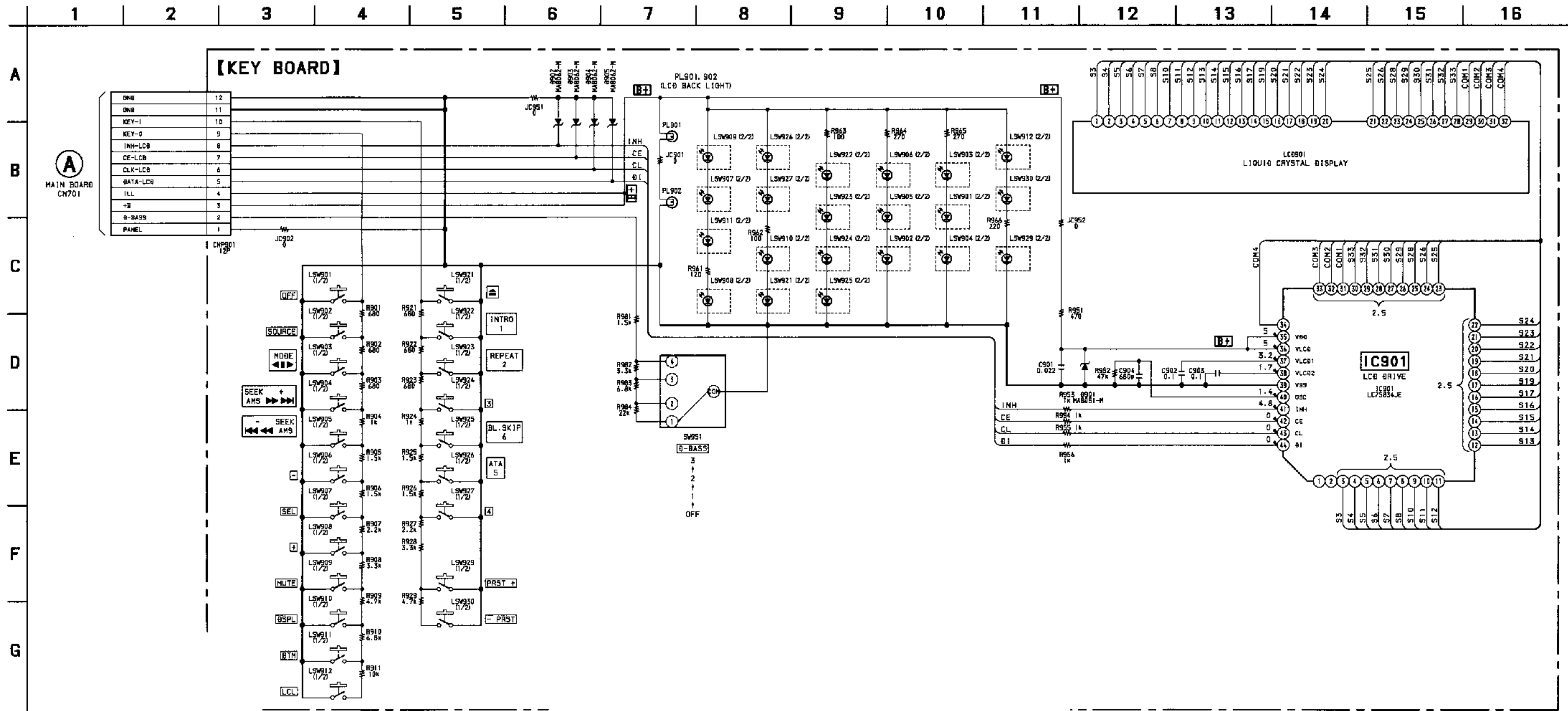
SCHEMATIC DIAGRAM - MAIN Section -



**Note on Schematic Diagram:**

- All capacitors are in  $\mu\text{F}$  unless otherwise noted.  $\text{pF}$ :  $\mu\text{F}$  50 WV or less are not indicated except for electrolytics and tantalums.
- All resistors are in  $\Omega$  and  $\frac{1}{4}\text{W}$  or less unless otherwise specified.
- $\Delta$ : Internal component.
- $\square$ : Panel designation.
- $\text{B}+$ : B+ Line.
- Power voltage is dc 14.4 V and fed with regulated dc power supply from ACC and BATT terminals.
- Voltages and waveforms are dc with respect to ground under no-signal (detuned) conditions.
- no mark: FM
- ( ): AM
- << >>: TAPE (PLAY)
- \*: Impossible to measure
- Voltages are taken with a VOM (input impedance 10 M $\Omega$ ). Voltage variations may be noted due to normal production tolerances.
- Waveforms are taken with an oscilloscope. Voltage variations may be noted due to normal production tolerances.
- Signal path.
- $\Rightarrow$ : FM
- $\Rightarrow$ : AM
- $\Rightarrow$ : TAPE (PLAY)
- $\Rightarrow$ : BUS AUDIO IN
- Abbreviation
- 105: EXR-105
- 3740: XR-3740

SCHEMATIC DIAGRAM – KEY Section –



Note on Schematic Diagram:

- All capacitors are in  $\mu\text{F}$  unless otherwise noted.  $\text{pF}$ :  $\mu\text{pF}$   
50 WV or less are not indicated except for electrolytics and tantalums.
- All resistors are in  $\Omega$  and  $\frac{1}{4}W$  or less unless otherwise specified.
- : panel designation.
- **B+** : B+ Line.
- Power voltage is dc 14.4 V and fed with regulated dc power supply from ACC and BATT terminals.
- Voltages are dc with respect to ground under no-signal (detuned) conditions.  
no mark : FM
- Voltages are taken with a VOM (input impedance 10 M $\Omega$ ).  
Voltage variations may be noted due to normal production tolerances.